Dr. K. C. NARASIMHAMURTHY

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Education

Sl. No.	Degree	Year	Institute	Specialization
1	B.E.	1992	SIT, Tumkur	Electronics
2	M.Tech.	1995	SJCE Mysore	Industrial Electronics
3	Ph.D.	2011	Indian Institute of Technology Guwahati	Microelectronics

Professional Experience

Sl. No.	Date (from-to)	Designation	Organization
1	2012 till date	Professor	SIT, Tumkur
2	2007-2012	Assistant professor	SIT, Tumkur
3	1995-2007	Lecturer	SIT, Tumkur

Positions held

- Head of the Department
- Coordinator of Mini projects, Analog Electronics Lab

Affiliations of Professional organizations

- ISTE

Awards and Honors

- BITES Best Thesis Award for 2011 in Electronics

Courses Taught

Undergraduate Courses

- Analog Electronic Circuits,
- Control Systems,
- Digital VLSI Design,
- Analog VLSI Design,
- Linear Integrated Circuits,

Research Guidance

Sl. No	Name of the Scholar	Title	Year of completion
1	M C Chandrashekar	Fabrication and Electrical Characterization of Single-walled Carbon Nanotube Based Thin Film Transistors	2019

Research Areas

- Microelectronics

Publications

Journals

- Performance comparison of single-and dual-gate carbon-nanotube thin-film field-effect transistors,KC Narasimhamurthy, R Paily, IEEE transactions on electron devices 58 (7), 1922-1927, 2011
- Fabrication of carbon nanotube field effect transistor, KC Narasimhamurthy, R Paily, IETE Technical Review 28 (1), 57-69,2011
- High-performance local back gate thin-film field-effect transistors using sorted carbon nanotubes on an amino-silane treated hafnium oxide surface, KC Narasimhamurthy, R Paily, Semiconductor science and technology 26 (7), 075002, 2011
- Performance comparison of interdigitated thin-film field-effect transistors using different purity semiconducting carbon nanotubes, KC Narasimhamurthy, RP Palathinkal, Advanced materials research 181, 343-348, 2011
- Fabrication and characterisation of high-performance and high-current back-gate thin-film field-effect transistors using sorted single-walled carbon nanotubes, KC Narasimhamurthy, R Paily, IET circuits, devices & systems 5 (5), 365-370, 2011
- Wafer scale thin-film transistors using different semiconducting purity nanotubes, dielectric materials and gate control, KC Narasimhamurthy, R Paily, Solid-state electronics 79, 37-44, 2013
- Fabrication, electrical characterization and mechanical flexibility test of back gated carbon nanotube thin film transistors on polyimide substrate, MC Chandrashekhar, KC Narasimhamurthy, Materials Research Express 5 (12), 126304, 2018

- Effect Of Semiconducting Purity Of Single-Walled Carbon Nanotubes On The Electrical Performance Of Flexible Carbon Nanotube Thin Film Transistors
  MC Chandrashekhar, KC Narasimhamurthy, International Journal of Electronics Engineering (ISSN: 0973-7383) Volume 11 • Issue 1 pp. 630-639 Jan 2019-June 2019
- M. C. Chandrashekhar & K. C. Narasimhamurthy (2019): Temperature dependent Electrical Characterization of Single and Dual-gate Flexible Carbon Nanotube Thin Film Transistors, IETE Journal of Research, DOI: 10.1080/03772063.2019.1620645, https://doi.org/10.1080/03772063. 2019.1620645 Published online: 23 May 2019. ISSN: 0377-2063
- Temperature-dependent Electrical Characterization of Single and Dual-gate Flexible Carbon Nanotube Thin Film Transistors, MC Chandrashekhar, KC Narasimhamurthy, IETE Journal of Research 68 (1), 703-713, 2022

Conference Proceedings

- Impact of bias voltage on magnetic inductance of carbon nanotube interconnects, KC Narasimhamurthy, RP Paily, 2009 22nd International Conference on VLSI Design, 505-510, 2009
- KC Narasimhamurthy, E Sai Kumar, K Likhitha, N Navya Chowdary, B Shilpa, N, Laxmi Sowmya, Ajay Shiva, "Analysis of Wave Shaping Circuits in Remote Lab", Published on 2019/2/3. International Conference on Remote Engineering and Virtual Instrumentation, Pages 605-611,Publisher Springer, Cham.
- KC Narasimhamurthy, TS Bindhu, Susheen Natraj, GC Bharath, Ankit Sharma, Ajay Shiva "Exploration of Common Emitter Amplifier in Remote Lab", In: Auer M., Ram B. K. (eds) Cyber-physical Systems and Digital Twins. REV2019 2019. Lecture Notes in Networks and Systems, , vol 80. Springer, Cham. https://doi.org/10.1007/978-3-030-23162-0_56
- K C Narasimhamurthy K.C., Thanmayi K.C., Bhuvana T.N., Chaitra H.R., Spurthy K.N., Raghavendra Kashyap T.M. (2020) Analysis of Filter Circuits in Remote Lab. In: Auer M., Ram B. K. (eds) Cyber-physical Systems and Digital Twins. REV2019 2019. Lecture Notes in Networks and Systems, vol 80. Springer, Cham. Published on 2019/2/3, https://doi.org/10.1007/978-3-030-23162-0_55
- K C Narasimhamurthy K.C. E. Sai Kumar,K. Likhitha,N. Navya Chowdary,B. Shilpa.N. Laxmi Sowmya,Ajay Shiva. Analysis of Wave Shaping Circuits in Remote Lab. In: Auer M., Ram B. K. (eds) Cyber-physical Systems and Digital Twins. REV2019 2019. Lecture Notes in Networks and Systems, vol 80. Springer, Cham. Published on 2019/2/3 https://doi.org/10.1007/978-3-030-23162-0_54
- KC Narasimhamurthy, KC Thanmayi, TN Bhuvana, HR Chaitra, KN Spurthy, TM Raghavendra Kashyap, Analysis of Filter Circuits in Remote Lab. In: Auer M., Ram B. K. (eds) Cyber-physical Systems and Digital Twins. REV2019 2019. Lecture Notes in Networks and Systems, vol 80. Springer, Cham. Published on 2019/2/3 https://doi.org/10.1007/978-3-030-23162-0_55

- KC Narasimhamurthy, Bharat Malaviya, Kondamarri Reddy Pranesh, Alladi Jayashree, Harikrishna Kamatham, Analysis of Operational-Amplifier Inverting and Non-inverting Amplifiers in Remote Lab. In: Auer M., Ram B. K. (eds) Cyber-physical Systems and Digital Twins. REV2019 2019. Lecture Notes in Networks and Systems, vol 80. Springer, Cham. , Published on 2019/2/3 https://doi.org/10.1007/978-3-030-23162-0_53
- Ankit Sharma, KN Spurthy, K Annapoorneshwari, Shorya Shubham, KC Narasimhamurthy, Development of Report Evaluation Portal for Remote Lab. In: Auer M., Ram B. K. (eds) Cyber-physical Systems and Digital Twins. REV2019 2019. Lecture Notes in Networks and Systems, vol 80. Springer, Cham. Published on 2019/2/3 https://doi.org/10.1007/978-3-030-23162-0_52,

Reviewer of Journal

- International Journal of Online Engineering

Invited Lectures, talks and workshops

Title	Programme	Venue	Date
Mobile Hands-On Learning	NIWeek 2017	Austin, USA	May 22 – 25, 2017
Electronic Circuits: Experiential Learning using Analog Discovery	NIDAYs 2017 Workshop	Bangalore	26 Oct 2017
Experiential Learning using Analog Discovery	FDP	Dept. of ECE GPCET, Karnool, AP	14-15 April 2018
Virtual/Remote lab on Analog Electronic Circuits Anyone | Anywhere | Anytime	FDP	Dept. ECE REVA	17 April, 2018
Virtual/Remote lab on Analog Electronic Circuits Anyone | Anywhere | Anytime	REV 2018	Dusseldorf, Germany	23 March 2018
DESIGN AND DEVELOPMENT OF REMOTE LAB "A Platform for Experiential Learning"	Conference Workshop	National Instruments at SRM Amaravathi AP	16July 2018
REMOTE LAB A PLATFORM FOR EXPERIENTIAL LEARNING	IUCEE Webinar Series	Internet	Oct.30, Nov 13, 20 and 27 2018

REMOTE LAB A PLATFORM FOR EXPERIENTIAL LEARNING on National Instrument Hardware platforms	NI Academic and Research Day	Chennai	4 Dec. 2018
Virtual Lab for Electronic Circuits	ICTIEE 2018, Workshop	Tyagaraja College of Engg. Madurai	2018
REMOTE LAB A PLATFORM FOR EXPEREINTIAL LEARNING	REV 2019, Workshop	BMSCE, Bangalore	Feb 2, 2019